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Application ID:

10723547

Title of Invention:

A Vertical Replacement-Gate

Junction Field-Effect Transistor

First Named Inventor:

Samir Chaudhry

Domestic/Foreign Application:

Domestic Application

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TRANSMITTAL

Eectronic Version v1.1
Stylesheet Version v1.1.0

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Submitted by:	Elec. Sign.	Sign. Capacity
John L. DeAngelis Jr. Registered Number: 30,622	/John L. DeAngelis, Jr./	Attorney

Documents being submitted

Files

us-ids

Agere285-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

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(5367184 or 5599724 or 5414289 or 5576238 or 5668391 or 5744846 or 6297531 or 4366495 or 4455565 or 4587713 or 4683643 or 4786953 or 4837606 or 5342797 or 6027975 or 6072216

or 6133099 or 6197641 or 5866925 or

5889298).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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	2	5599724	1997-02-04	Yoshida			
	3	5414289	1995-05-09	Fitch, et al			
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	5	5668391	1997-09-16	Kim, et al			
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	7	6297531	2001-10-02	Armacost, et al			
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Signature

Examiner Name	Date